

REMARKS

Reconsideration of this application, as presently amended, is respectfully requested. Claims 1, 2, 7, 8, 13, 15 and 16 are now pending in this application, new claim 16 having been added by the present amendment. Claim 7 has been allowed. Claims 3-6, 9-12 and 14 have been cancelled. Claims 1, 2, 8, 13 and 15 stand rejected.

Claim Rejections - 35 U.S.C. § 112

Claim 15 was rejected under 35 U.S.C. § 112, second paragraph, for indefiniteness. Specifically, the Examiner asserts that “the first logic level” in line 3 of claim 15 lacks proper antecedent basis.

Claim 15 has been amended to obviate the § 112, second paragraph rejection. Reconsideration and withdrawal of the rejection under § 112, second paragraph, are respectfully requested.

Claim Rejections - 35 U.S.C. § 102

Claims 1-2, 8, 13 and 15 were rejected under 35 U.S.C. § 102(b) as being anticipated by **Freeman** (RE 34,363). For the reasons set forth in detail below, this rejection, to the extent it is considered to apply to the amended claims, is respectfully traversed.

Claims 1, 2 and 15 have been amended to further clarify the structural differences between **Freeman** and the claimed invention. More specifically, claim 1 has been amended to define the transistor structure of the transmission section as having electrically connected

transistors that receive the inverted first input signal and the inverted second input signal and output one of these signals in response to an externally controllable selection signal and the inverted signal of the selection signal. Claims 2 and 15 have been amended similarly to claim 1.

Unlike the claimed invention, the transistors connected to inputs C2 and C3 of **Freeman** are not electrically connected. Specifically, the transistors connected to C2 and C3 are separate pass transistors that are not connected electrically.

In view of the above amendments and remarks, it is respectfully submitted that independent claims 1, 2 and 15, and dependent claim 8, which depends from claim 1, patentably distinguish over the cited prior art and are in condition for allowance. Reconsideration and withdrawal of the rejection of these claims under § 102 are respectfully requested.

Claim 13 has been amended to clarify features of the invention.

The **Freeman** reference clearly does not disclose or suggest the circuit structure of the first and second inversion sections, as recited in claim 13, including transistor circuits (plural), each transistor circuit having a first input signal terminal for the first input signal. Further, **Freeman** does not disclose or suggest the transmission section, as recited in claim 13, including transistor circuits, each of said transistor circuits having a first selection signal terminal for inputting the controllable selection signal and a second selection signal terminal for inputting the inverted signal of the selection signal.

Freeman does not disclose a specific transistor structure. The Examiner asserts that the basic logic circuits 21, 22 are composed of transistors (see Office Action, page 7, lines 3-4). While it is reasonable to assert that the logic gates 21, 22 may include transistors, there is

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absolutely no disclosure or suggestion that *each transistor* includes an input signal terminal for an input signal.

In view of the above amendments and remarks, reconsideration and withdrawal of the rejection of claim 13 under 35 U.S.C. § 102 are respectfully requested.

New Claim 16

New claim 16 has been added. New claim 16 is similar to claim 13 and more specifically defines the first inversion section, second inversion section and a transmission section as comprising CMOS logic circuits. It is respectfully submitted that claim 16 is allowable for the same reasons set forth above with respect to claim 13, and for the additional reason that the **Freeman** reference does not disclose or suggest CMOS logic circuits.

CONCLUSION

In view of the aforementioned amendments and accompanying remarks, Applicants submit that the claims, as herein amended, are in condition for allowance. Applicants request such action at an early date.

If the Examiner believes that this application is not now in condition for allowance, the Examiner is requested to contact Applicants' undersigned attorney to arrange for an interview to expedite the disposition of this case.

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If this paper is not timely filed, Applicants respectfully petition for an appropriate extension of time. The fees for such an extension or any other fees that may be due with respect to this paper may be charged to Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP



William M. Schertler
Attorney for Applicants
Registration No. 35,348
Telephone: (202) 822-1100
Facsimile: (202) 822-1111

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